

**ESPA**

Electronic Engineering Internship _IC Design

(PRAEED0912)

Apply here

Start date

April 2022

Duration

6 months

Languages

Good spoken and written English levels are required (B2 onwards)

Location

Cambridge, England
Home to the world-famous University of Cambridge, dating back to 1209, this historical city has beautiful architecture and majestic college buildings aplenty. With fascinating museums, atmospheric pubs, fine dining, incredible street food and ancient colleges all jostling together in the city centre, as well as the beautiful riverside and open green spaces, you are guaranteed a great experience.

Are you eligible?

Are you a registered student?

Or

Are you eligible to participate in the Erasmus+ programme?

Benefits

See website for details of all ESPA benefits. For all internships over 6 months, additional benefits will be paid. Details available at interview.

Role

This is a fantastic opportunity for an electronics engineering student to gain hands on experience, at a world leader in ultra-low-cost flexible electronics. Mentored throughout by the Emerging Applications Team, you will be given an existing core list of functioning IP (Intellectual Property) Blocks and using given spec sheets you will develop higher level functioning blocks to fulfil a purpose on larger systems, using electronic design tools.(ECAD). This will be a great work experience to learn from the experts and develop your skills and knowledge

Tasks

- From an IP Design Requirements document, generate a specification/solution for IC design that meet the requirements of the customer
- Write HDL Verilog (Hardware Design Language) files, that implement the functionality of that circuit
- Write testbenches that validate that the circuit design behaves as expected
- Using Cadence Tools run through the workflow to turn the HDL code into an IC design, including simulation and extracting key performance metrics
- Continuously documenting the design progress, preparing for design reviews

Preferred Skills

- Studying for a degree in Electronics Engineering or similar
- Knowledge of transistor and component level electrical circuit design
- Must have used IC design tools such as schematic & layout design, SPICE simulation
- Experience of using the Linux operating system
- Experience of hardware description language such as Verilog
- Ability to document steps meticulously
- Methodical mindset and proactive communicator not afraid to ask questions

The Host Company

This award-winning host's novel products, are being adopted by a growing base of global companies across diverse markets, including consumer goods, games, retail, pharmaceutical and security. With a billion-unit production facility, the host company's unique, patented technology platform opens-up the opportunity to invent entirely new applications for electronics. Their mission is to create more connectivity, create more designs and create more devices. With staff from over a dozen countries, covering 5 continents, the company culture promotes an open and collaborative environment, committed to delivering a new generation of electronics to address real world issues.